Serial Number: 10/812,277

Filing Date: March 29, 2004

Title: SEMICONDUCTING DEVICE WITH FOLDED INTERPOSER

Assignee: Intel Corporation

As part of maintaining the restriction, the Examiner states at page 2 of the Office Action that "the product can be made by the following methods: a)the second die could be secured to a second surface of the interposer before the interposer is folded." Applicant can not see where the statement adequately indicates that claims 1-7 and 18-20 are distinct inventions from claims 8-17. Clarification is respectfully requested.

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In addition, Applicant also fails to see the relevance of the statements because as stated above, the Examiner will be looking for an interposer, a contact and first and second dies as recited in each of claims 1-20. Applicant notes that the Examiner will be searching for each of these items without regard to when the interposer is folded.

As part of maintaining the restriction of the claims in Group I, the Examiner states at page 3 of the Office Action that none of the claims in Group I are generic. Applicant respectfully can not see where the statement adequately indicates that claims 1-7 and 18-20 are distinct inventions. Applicant respectfully requests clarification.

Applicant notes that all of the pending claims in Group I (1-7 and 18-20) relate to a semiconducting device and an electronic system that include (i) an interposer having a fold which divides the interposer into a first section and a second section; (ii) a first die and a contact that are each attached to the first surface of the interposer at the first section and the second section; and (iii) a second die attached to a second surface of the interposer where the second die is stacked onto the first die and electrically coupled to the first die by the contact and the interposer. The Examiner will be looking for an interposer, a contact and first and second dies as recited in each of claims 1-7 and 18-20. Thus, Applicant respectfully submits that these claims can all be easily searched and examined together.

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RESPONSE TO RESTRICTION REQUIREMENT

Serial Number: 10/812,277 Filing Date: March 29, 2004

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Assignee: Intel Corporation

The Examiner is invited to telephone Applicants' attorney Andrew Peret at 262-646-7009, or the below-signed attorney at 612-349-9592, to facilitate prosecution of this application. If necessary, please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Respectfully submitted,

IWEN CHAO ET AL.

By their Representatives,

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(612) 349-9592

Date dug. 25, 2005

Ann M McCrackin

Reg. No. 42,858

<u>CERTIFICATE UNDER 37 CFR 1.8:</u> The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450,

Alexandria, VA 22313-1450 on this 22 day of August, 2005.

Name

Signature